

## TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

Patent application of:	Stephen Melvin Enrique Musoll Mario D. Nemirovsky Nandakumar Sampath Hector Urdaneta
Serial No.:	09/706157
Filed:	11/03/00
Docket:	MIPS.0171-00-US
For:	CLUSTERING STREAM AND/OR INSTRUCTION QUEUES FOR MULTI-STREAMING PROCESSORS

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

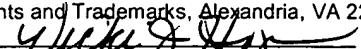
A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

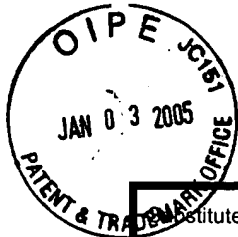
Respectfully submitted,

  
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By: 



<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	09/706157
				Filing Date	11/03/00
				First Named Inventor	Stephen Melvin Enrique Musoll Mario D. Nemirovsky Nandakumar Sampath Hector Urdaneta
				Group Art Unit	2183
				Examiner Name	Eric Coleman
Sheet	1	of	3	Attorney Docket Number	MIPS.0171-00-US

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup>			
	AA	4916652		Schwarz et al	04/10/1990	
	AB	5404469		Chung et al	04/04/1995	
	AC	5574939		Keckler et al	11/12/1996	
	AD	5742782		Ito et al	04/21/1998	
	AE	5745725		Robert John Simpson	04/28/1998	
	AF	5812811		Dubey et al	09/22/1998	
	AG	5913049		Shiell et al	06/15/1999	
	AH	5933627		Bodo Parady	08/03/1999	
	AI	6092175		Levy et al	07/18/2000	
	AJ	6105127		Kimura et al	08/15/2000	

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number <sup>4</sup>	Kind Code <sup>5</sup>			


Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3).

<sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	09/706157
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		Group Art Unit	2183
		Examiner Name	Eric Coleman
Sheet	2	of	3
		Attorney Docket Number	MIPS.0171-00-US

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	BA	<i>The PowerPC Architecture: A Specification for a New Family of RISC Processors</i> , Second Edition, Morgan Kaufmann, San Francisco, pp.70-92 (May 1994) ✓	
	BB	<i>MC68020 32-Bit Microprocessor User's Manual</i> , Third Edition, Prentice Hall, New Jersey, pp. 3-125, 3-126, and 3-127 (1989) ✓	
	BC	Potel, M.J., "Real-Time Playback in Animation Systems," <i>Proceedings of the 4<sup>th</sup> Annual Conference on Computer Graphics and Interactive Techniques</i> , San Jose, CA, pp. 72-77 (1977) ✓	
	BD	<i>ARM Architecture Reference Manual</i> , Prentice Hall, pp. 3-41, 3-42, 3-43, 3-67, 3-68 (1996) ✓	
	BE	<i>ESA/390 Principles of Operation</i> , IBM Library Server, Table of Contents and Para.7.5.31 and 7.5.70 (1993) (available at: <a href="http://publibz.boulder.ibm.com/cgi-bin/bookmgr_OS390/BOOK/DZ9AR001/CCONTENT">http://publibz.boulder.ibm.com/cgi-bin/bookmgr_OS390/BOOK/DZ9AR001/CCONTENT</a> )	
	BF	<i>MC88110 Second Generation RISC Microprocessor User's Manual</i> , Motorola, Inc., pp.10-66, 10-67, and 10-71 (1991) ✓	
	BG	Diefendorff, Keith et al., "Organization of the Motorola 88110 Superscalar RISC Microprocessor," <i>IEEE Micro</i> , Vol, 12, No. 2, pp.40-63 (1992) ✓	
	BH	Kane, Gerry, <i>PA-RISC 2.0 Architecture</i> , Prentice Hall, New Jersey, pp. 7-106 and 7-107 (1996) ✓	
	BI	Diefendorff, Keith et al, "Altivec Extension to PowerPC Accelerates Media Processing," <i>IEEE Micro</i> , Vol, 20, No. 2, pp.85-95 (March-April 2000) ✓	
	BJ	Diefendorff, Keith, "WinChip 4 Thumbs Nose at ILP," <i>Microprocessor Report</i> , Vol, 12, No.16, 10 pages (December 7, 1998) ✓	
	BK	Slater, Michael, "Rise Joins x86 Fray with mP6," <i>Microprocessor Report</i> , Vol, 12, No. 15, 10 pages (November 16, 1998) ✓	
	BL	Diefendorff, Keith, "Jalapeno Powers Cyrix's M3," <i>Microprocessor Report</i> , Vol. 12, No. 15, 11 pages (November 16, 1998) ✓	
	BM	Becker, Michael C., et al, "The PowerPC 601 Microprocessor," <i>IEEE Micro</i> , Vol, 13, No.5, pp.54-68 (October 1993) ✓	

Examiner Signature		Date Considered	
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Sheet 3 of 3

## OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

Examine r Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	BN	Diefendorff, Keith, "Compaq Chooses SMT for Alpha," <i>Microprocessor Report</i> , Vol, 13, No. 16, 13 pages (December 6, 1999) ✓	
	BO	Eggers, Susan J., et al., "Simultaneous Multithreading: A Platform for Next-Generation Processors," <i>IEEE Micro</i> , Vol, 17, No. 5, pp. 12-19 (September/October 1997) ✓	
	BP	Gwennap, Linley, "Digital 21264 Sets New Standard," <i>Microprocessor Report</i> , Vol, 10, No. 14, 11 pages (October 28, 1999) ✓	
	BQ	Diefendorff, Keith, "Power4 Focuses on Memory Bandwidth," <i>Microprocessor Report</i> , Vol, 13, No. 13, 13 pages (October 6, 1999) ✓	
	BR	Diefendorff, Keith, "K7 Challenges Intel," <i>Microprocessor Report</i> , Vol, 12, No. 14, 7 pages (October 26, 1998) ✓	

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